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Substitute for form 1449/PTO

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Application Number	10/075,149
Filing Date	February 14, 2002
First Named Inventor	Milivoje Aleksic
Art Unit	2111
Examiner Name	Justin King
Attorney Docket Number	00100.02.0060

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Sheet 1 of 4

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		US- 4,920,504	4-1990	Sawada et a.	
		US- 5,384,737	1-1995	Childs et al.	
		US- 5,600,605	2-1997	Schaefer	
		US- 5,602,986	2-1997	Trevett	
		US- 5,657,469	8-1997	Shimizu	
		US- 5,752,269	5-1998	Divivier et al.	
		US- 5,761,727	6-1998	Wu et al.	
		US- 5,767,865	6-1998	Inoue et al.	
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		US- 5,850,483	12-1998	Takabatake et al.	
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		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
		EP 0829820 A2	3-18-1998	Silicon Graphic		
		WO97/06523	2-20-1997	Cirrus Logic		

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/Khanh Dang/ (01/22/2009)

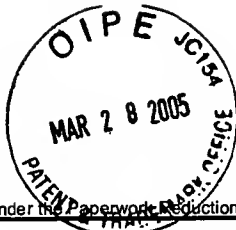
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PTO/SB/08A (08-03)

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		Number-Kind Code ² (if known)			
		US- 6,041,417	3-2000	Hammond et al.	
		US- 6,052,134	4-2000	Foster	
		US- 6,052,756	4-2000	Barnaby et al.	
		US- 6,076,139	6-2000	Welker et al.	
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		US- 6,151,651	11-2000	Hewitt et al.	
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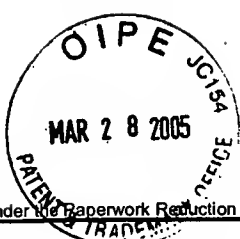
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		US- 6,317,803 B1	11-2001	Rasmussen et al.	
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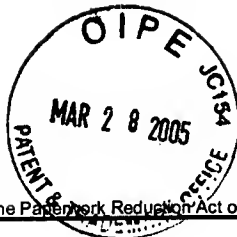
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		Examiner Name	Justin King		
Sheet	4	of	4	Attorney Docket Number	00100.02.0060

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		European Search Report for Patent Application EP 0030 4528, published May 3, 2002.	
		MITSUYAMA et al.; VLSI Implementation of High Performance Burst Mode for 128 Bit Block Ciphers; IEEE 2001	

Examiner Signature	/Khanh Dang/ (01/22/2009)	Date Considered	
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